Approved For Relea

NPIC/TDS-201/67 1 November 1967

SUBJECT: Amplifications on Chip Comparator and Its Operational Reliability	25X1
REFERENCE: NPIC/TDS/D-1091-67 dated 24 October 1967, Subject Chip Comparator Progress Report	25X1 25X1
1. In relation to the above referenced memorandum, TDS would like to clarify the operational circumstances under which the comparator can be considered reliable. It is our belief that the basic technical difficulties with the comparators have been resolved based on the performance record of the prototype comparator now operating in IAS and the pre-delivery inspection of the two production comparators now under	
test	25X1

- 2. At this time there is no reason to believe that these machines can not be successfully operated within the NPIC as long as the following services are provided:
 - a. That the comparators be installed in the proper environmental conditions as specified on the installation engineering form previously supplied the office of logistics. i.e. approximately $70^{\rm OF}$ \pm $5^{\rm OF}$ and 55% R.H. \pm 10% with reasonable air filtration. There is no reason to believe that building services cannot provide these conditions.
 - b. That IPD provide adequate software and computer time to meet operational requirements. Circumstances already permit on-line operation of the chip comparator presently in IAS, but the services are not as extensive as originally envisioned in the chip system concept. There is no reason that additional services cannot be provided by IPD, but it is up to the individual components to levy the requirements on IPD.
 - c. Operator training is a desirable additional feature that should be considered by components that do not have experienced

Declass Review by NIMA / DoD

MEMORANDUM FOR THE RECORD

SUBJECT: Amplification Operational	ns on Reliability	Chip Comparator and Its	25X1
anvone qualified .	to operate a C	ions. In a personal sense Comparator can be thoroughly r in less than one hour.	25X1
comparators cannot be future, but since there	considered operations on the considered operations of the component to request to reques	eseeable reasons why the chip onally acceptable in the near with complete system responsi st any necessary building serv may desire.	bility ices,
			25X1
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